

Debugging 100G Access Switch in Nepal



Article Overview

To debug a 100G access switch, verify interface configuration, FEC settings, SFP compatibility, and monitor traffic using built-in hardware visualization tools.

Key Steps for Debugging

1. **Verify Physical Connectivity and SFP Modules** Ensure that the 100G SFP+ or QSFP modules are correctly seated and compatible with the switch model. Check that the correct media type (DAC, AOC, or fiber) is configured for each port . Inspect LEDs and port status indicators to confirm link integrity.
2. **Check Forward Error Correction (FEC)** FEC must be enabled on both ends of a 100G link. Mismatched FEC settings can prevent the interface from coming up or cause high error rates . Use the switch CLI or GUI to confirm FEC status.
3. **Interface Configuration and Status** Use CLI commands to display interface status, speed, duplex, and error counters. Look for CRC errors, packet drops, or mismatched MTU settings. Ensure that the interface is administratively up and that VLANs or VXLAN overlays are correctly assigned .
4. **Traffic Monitoring and Visualization** High-end 100G switches like the HSGQ-9032 support hardware-based traffic visualization using protocols such as RSPAN and sFlow. These tools allow you to monitor per-port traffic, detect congestion, and identify bottlenecks in real time .
5. **Redundancy and Power Checks** Verify that redundant power supplies and fans are operational. Hot-swappable modules should be tested to ensure failover works without packet loss . Check logs for over-current, over-voltage, or over-heat events.
6. **Layer 2/3 Troubleshooting** For managed switches, confirm MAC address tables, spanning tree status, and link aggregation (M-LAG) configurations. Misconfigured M-LAG or VLANs can cause traffic blackholing or loops .
7. **End-to-End Testing** Use a test device or laptop with a 100G-capable NIC to...

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Python debugging in VS Code

Local computer: switch to the Run and Debug view (⇧⌘D (Windows, Linux Ctrl+Shift+D)) in VS Code, select the Python Debugger:

Catalyst 2960-S and 2960-C Switches Debug Commands

This appendix describes the debug privileged EXEC commands that have been created or changed for use with the Catalyst switch.

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Layer 1 Debug Guide

When debugging, always go inward approach for analyzing the PHY command output. In this section, we will discuss, the possible

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